

INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO-1449	Attorney Docket No. 2885/85	Serial No. 10/791,501
	Applicant(s) Vorbach et al.	
	Filing Date June 18, 2004	Group Art Unit 2116

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE

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EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	*Abnous, A. et al., "Ultra-Low-Power Domain-Specific Multimedia Processors," U.C. Berkeley, 1996 IEEE, pp. 461-470.
	*Sutton et al., "A Multiprocessor DSP System Using PADDI-2," U.C. Berkeley, 1998 ACM, pp. 62-65.
	*Zhang, et al., "Abstract: Low-Power Heterogeneous Reconfigurable Digital Signal Processors with Energy-Efficient Interconnect Network," U.C. Berkeley, pp. 1-120.
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* = Newly-Cited Reference (June 13, 2008)

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	5,361,373	November 1, 1994	Gilson			
	5,392,437	February 21, 1995	Matter et al.			
	5,737,516	April 1998	Circello et al.			
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	5,926,638	July 1999	Inoue			
	5,960,200	September 1999	Eager et al.			
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	0 463 721	January 2, 1992	EPO				
	0 539 595	May 5, 1993	EPO				
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	0 748 051	December 11, 1996	EPO				
	1 061 439	December 20, 2000	EPO				
	199 26 538	December 14, 2000	Germany			Abs.	
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